



# Max Stopyra

Engineer · Musician  
he/him

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## Education:

### Olin College of Engineering

Needham MA - Aug. 2020 - May 2024

**B.S. in Electrical and Computer Engineering**

Signals and Systems, Software Design, VLSI, Power Electronics, PCB Fab and Design

### Victoria University of Wellington

Wellington NZ - Jan. 2023 - June 2023

**Minor in Jazz Studies and Music Production**

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## Experience:

### SFQ Circuit Design Engineering Intern

MIT Lincoln Laboratories - Lexington, MA  
May 2022 - Sept. 2022

Use **Cadence** and **Virtuoso** to design and test single flux quantum circuitry for use in **quantum computing nanosystems**

Create **LISP** software tools to bolster the efficiency of single flux quantum **integrated circuit design** process

### Founder and Technical Lead

Aardvark Audio - Needham, MA  
Sept. 2021 - Present

Design, manage, and build music tech venture from the ground up as an **electrical engineer and entrepreneur**

Scaffold business model, design and create products that meet stakeholder needs

### Telematics Test Engineering Intern

Nissan - Farmington Hills, MI  
Oct. 2020 - Sep. 2021

Conduct end-to-end tests on **EV telematics systems using TestRail** and in-house software tools, ensuring efficient functionality for end-users, optimizing system architecture for new EV related infrastructure

**Design and improve hardware** used in static testing of prototypes and production-level telematics systems

## Projects:

### 3D-Gesture MIDI Controller: 3ddie

Design and create a standalone midi controller using capacitive gesture sensing in **KiCad** and **Altium**

**More info:** [github.com/smilg/3ddie](https://github.com/smilg/3ddie)

### Boston Food Forest Coalition Renewable Energies Integration

Senior engineer on PInT at Olin College designing and installing a modular solar system and rainwater collection system for one of Boston's food forests.

### FPGA-Based DAC

Design and implement a simple sigma-delta digital-to-analog converter in **SystemVerilog**

**More info:**

[github.com/mstopyra/FPGA\\_SIGDEL\\_DAC](https://github.com/mstopyra/FPGA_SIGDEL_DAC)

### Analog and Digital VLSI Work

Learn **CMOS IC design** through a series of projects

**More info:**

[github.com/mstopyra/MADVLSI\\_FA23](https://github.com/mstopyra/MADVLSI_FA23)

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## Skills & Interests:

### PCB Design & Fab:

KiCad, Eagle, Spice

Design

Jazz

### Programming:

Rust, C, C++, MATLAB, Python, LISP, Verilog, SystemVerilog,

Modular Synthesis

Audio Engineering

Bass

### VLSI: Magic,

Cadence Suite, LVS, Xschem, Spice

Sustainable Beekeeping

Backpacking

Embedded Systems

Eagle Scout

Logic Design

Sustainable Gardening

Quantum Computing

Birding

Renewable Energy

Sustainability

**Languages:**  
Spanish

Controls

Signal Processing